

DETAILS

Name: **Ben Pope** email ben@pope.name
Nationality: British Website www.ben.pope.name
Location: West Midlands, UK Mobile: 0797 005 1 004

SKILLS

Technologies

Web: HTML & CSS, JSP, XML (XSLT, XPath), Cocoon
Programming: ISO C++, ANSI C, Java
Other: SQL, JDBC, UML, Matlab, Simulink
Automotive: CAN, Time Triggered CAN, LIN, Flexray

Software

Office: Microsoft Office, OpenOffice
Imaging: Photoshop, Paint Shop Pro, GIMP
OS: Windows (various), Linux, Solaris

EDUCATION

University of Warwick Coventry, CV4 7AL

Degree Achieved July 2003:

Master of Engineering (with Honours) in
Computer Systems Engineering

Class two, Division one

(Examined subjects include: Computer Organisation and Architecture, Design of Information Structures, Maths for Engineers, Java, Signal Processing, Digital Signal Processing, Real Time Systems, Data Comms. & Networks, Database Systems, VLSI Architectures & Algorithms, Computer Graphics, Artificial Intelligence, Control, Analogue & Digital Systems Design, Optical Engineering, Finance & Accounting and Engineering Business Management)

Park College Eastbourne, East Sussex, BN21 2UN

A-Levels Achieved August 1999:

Mathematics	A	C	Computing
Physics	B		

Willingdon Secondary School Eastbourne, East Sussex, BN20 9QX

GCSEs Achieved August 1997:

Mathematics	A	B	English Language
Science	AA	B	English Literature

+5 Other passes

EXPERIENCE

Warwick Control Technologies

I currently hold the position of Product Development Manager. I am closely involved in the complete product lifecycle from requirements capture to maintenance.

Major projects so far have included:

Development of a Time Triggered Protocol (TTCAN) on top of CAN using the Atmel T89C51CC01 microcontroller, and used this to demonstrate bus loadings in excess of 90% with deterministic latency to within 2 bit times.

Development of a LIN stack compliant to versions 1.3 and 2.0, using the Infineon TriCore family of microcontrollers, written in C.

Currently creating a network analysis tool for Windows in ISO C++.

Formula Student

My final year project was a multidisciplinary group project of 12 people working together to design and build a formula-style racing car to compete in the Formula Student Competition representing Warwick University.

As technical lead in the electronics team, I designed and implemented an architecture to provide a flexible, modular and scalable data acquisition system based on homogenous CAN nodes.

I was one of three people delivering all electrical and electronic systems on the car. I worked closely with my two colleagues, contributing to ECU hardware design, leaving them to finalise the designs and manufacture the hardware. I also made key contributions within the chassis and power train groups.

I was solely responsible for the object oriented design and implementation of the software in C on the 8051-based microcontrollers in each CAN node. Each node is fully configurable from a remote workstation. I oversaw the development of a LabView application for data monitoring.

Other

Employment by the department of computer science at Warwick University, where I assisted in the teaching of a hardware undergraduate course. I found it extremely interesting helping fellow students to understand the problems and difficulties they faced in learning new ideas and concepts.

Through the Formula Student projects, I have become interested in many aspects of embedded control systems using microcontrollers, especially within motorsport and related industries.

Other interests include model aircraft flight and rock climbing.

Available on Request

OTHER INTERESTS

REFERENCES